



Marked out of 2.00

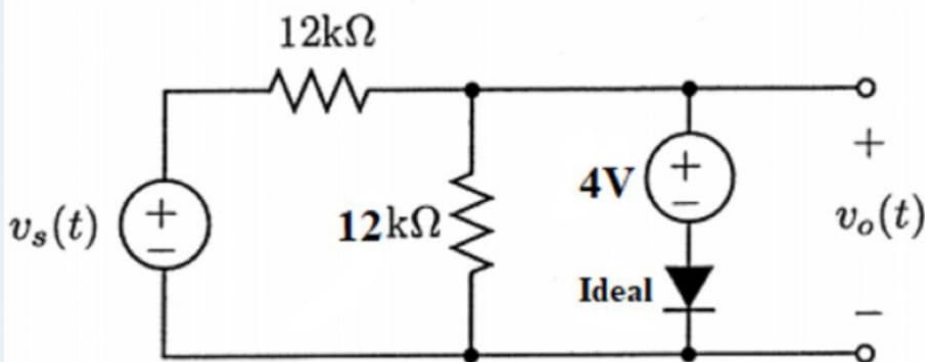
Time left 0:56:15

Flag question

For the circuit shown , when the diode is on

$V_o(t) =$ ∇ V for $V_s(t)$

∇ V



Question 2

Not yet answered

Marked out of 3.00

Flag question

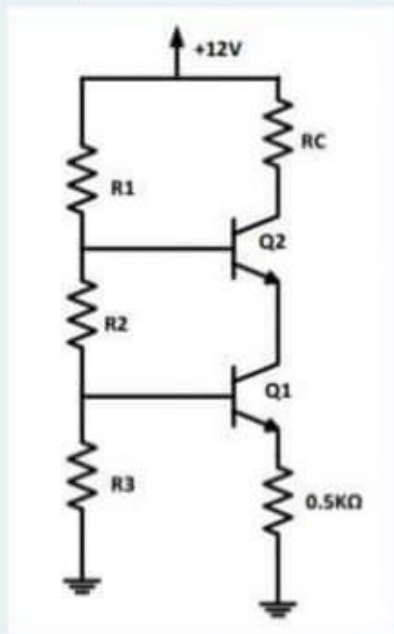
The BJT Transistors shown are in the active region and have $\beta = \infty$. Complete the design of the circuit such that $I_{C2} = 2\text{mA}$, and $V_{CE1} = V_{CE2} = 2\text{V}$. Let $R1+R2+R3 = 100\text{K}\Omega$.

1. $R_C =$ $\text{K}\Omega$

2. $R_1 =$ $\text{K}\Omega$

3. $R_2 =$ $\text{K}\Omega$

4. $R_3 =$ $\text{K}\Omega$

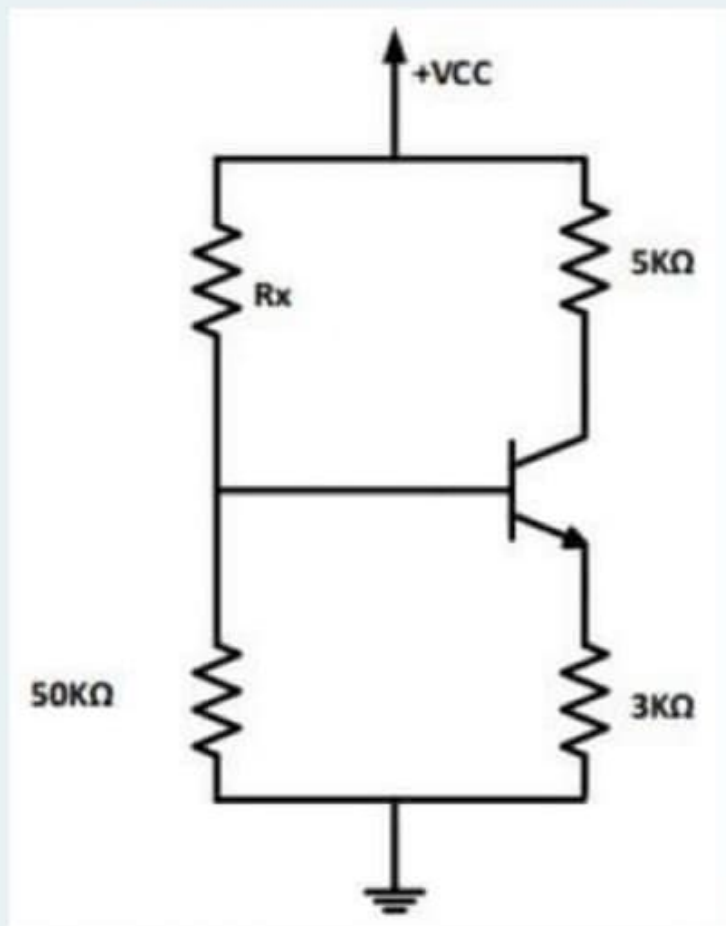


The BJT transistor in the circuit shown is in the active region and has $\beta = 100$. Complete the design of the circuit so that

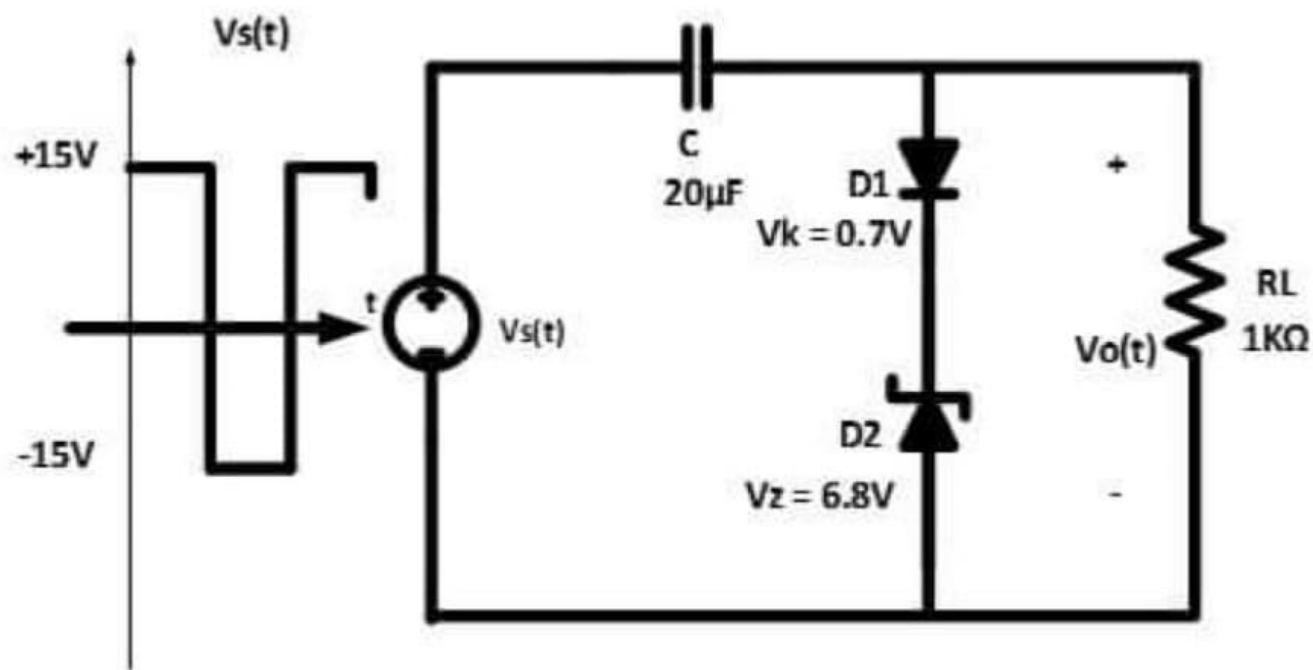
$I_C = 1.28\text{mA}$, and $V_C = 8.6\text{V}$

1. $R_x =$ $\text{K}\Omega$

2. $V_{CC} =$ V

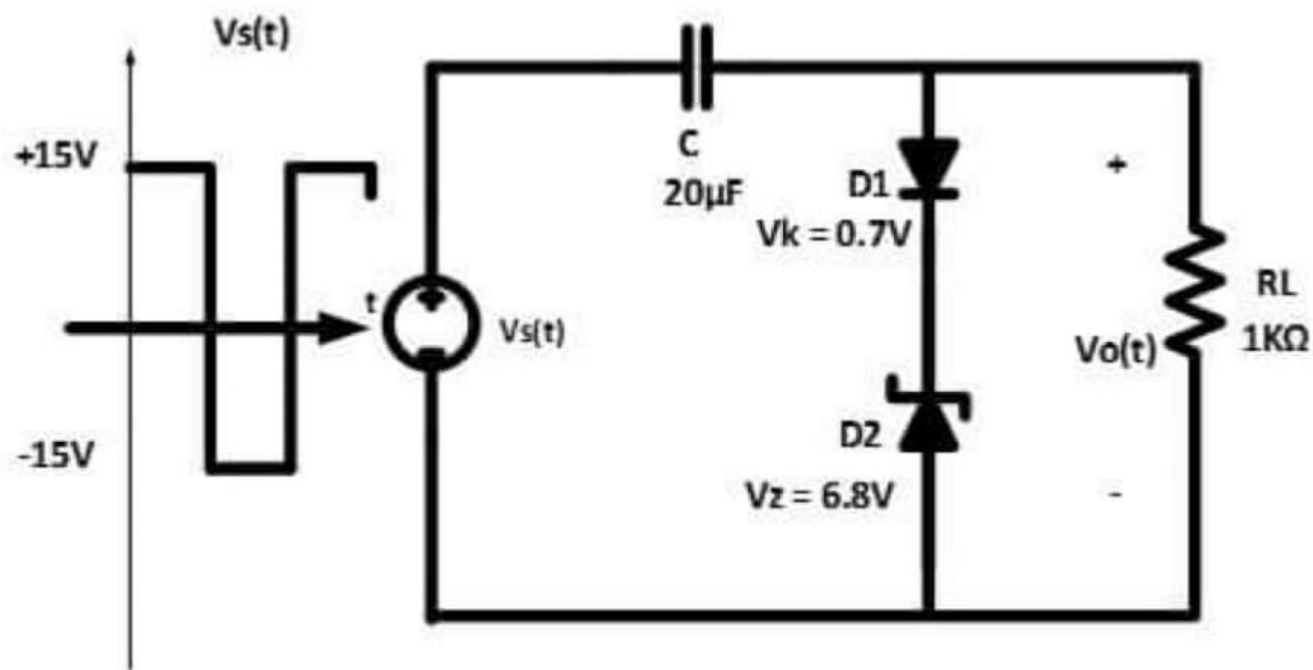


For the circuit shown, what is the minimum value of the output voltage $V_o(t)$.



- a. None of the listed values
- b. -14.2V.
- c. -22.5V.
- d. -6.8V.
- e. -30V
- f. -9.1V.
- g. -15V

For the circuit shown, what is the minimum value of the output voltage $V_o(t)$.



- a. None of the listed values
- b. -14.2V .
- c. -22.5V .
- d. -6.8V .
- e. -30V
- f. -9.1V .
- g. -15V

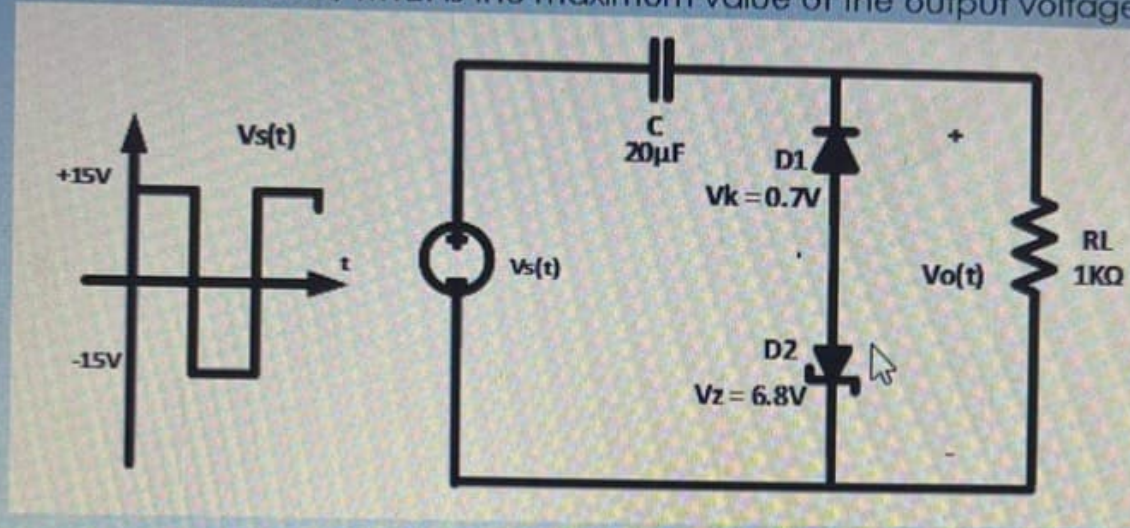
Question 3

Correct

Mark 3.00 out of 3.00

Flag question

For the circuit shown, what is the maximum value of the output voltage $V_o(t)$.



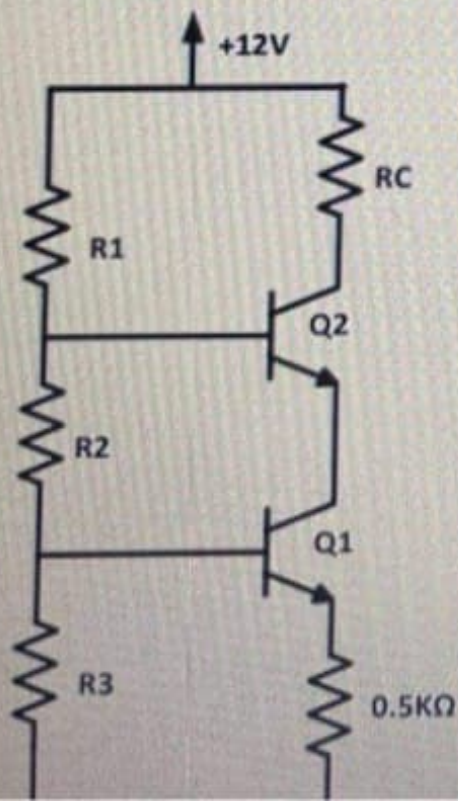
- a. 14.2V.
- b. 22.5V.
- c. +30V
- d. 9.1V.
- e. None of the listed values
- f. 6.8V.
- g. -15V

Type here to search



The BJT Transistors shown are in the active region and have $\beta = \infty$. Complete the design of the circuit such that $I_{C2} = 1\text{mA}$, and $V_{CE1} = V_{CE2} = 3\text{V}$. Let $R1 + R2 + R3 = 100\text{K}\Omega$.

- 1. $R_C =$ \times $\text{K}\Omega$
- 2. $R_1 =$ \checkmark $\text{K}\Omega$
- 3. $R_2 =$ \checkmark $\text{K}\Omega$
- 4. $R_3 =$ \checkmark $\text{K}\Omega$



f. 6.8V.

g. -15V

Question 4

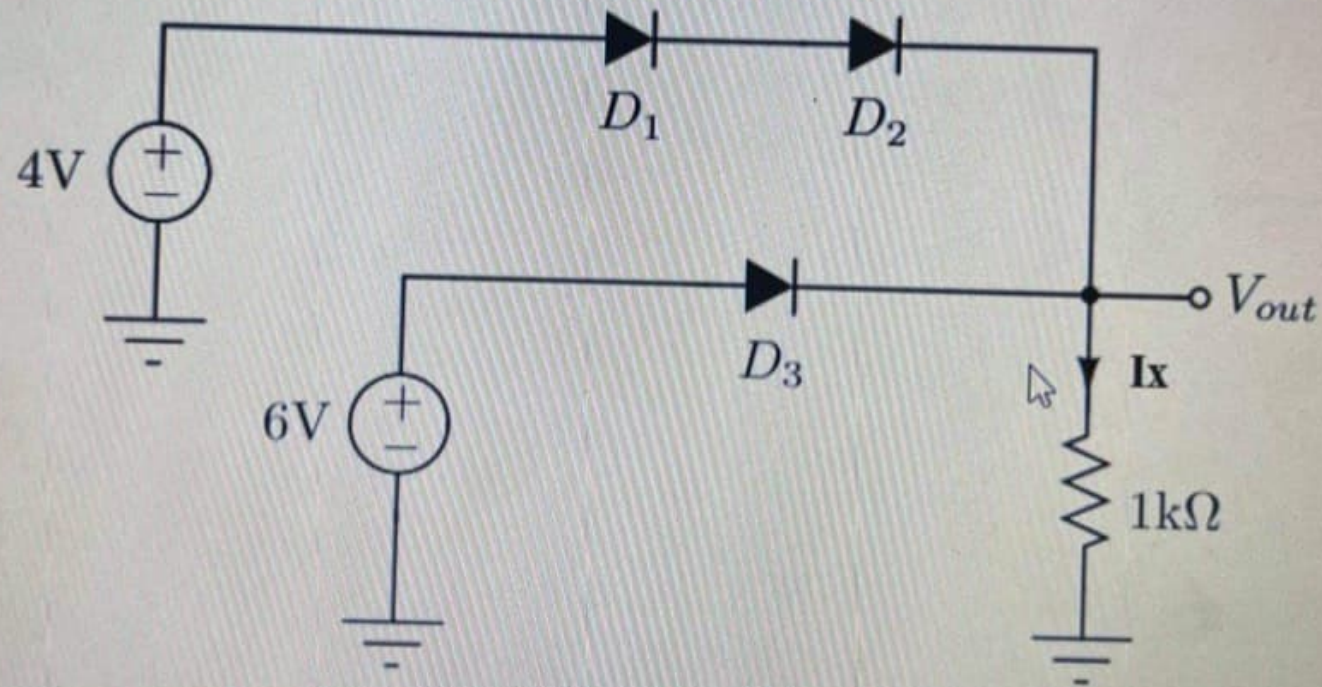
Correct

Mark 3.00 out of 3.00

Flag question

The diodes in the circuit shown have $V_K = 0.7V$

$I_x =$ mA



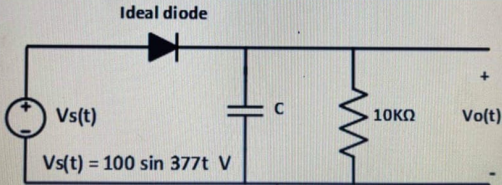
For the circuit shown, the value of the capacitor C that will result in a peak to peak ripple of 4V is

41.67

÷

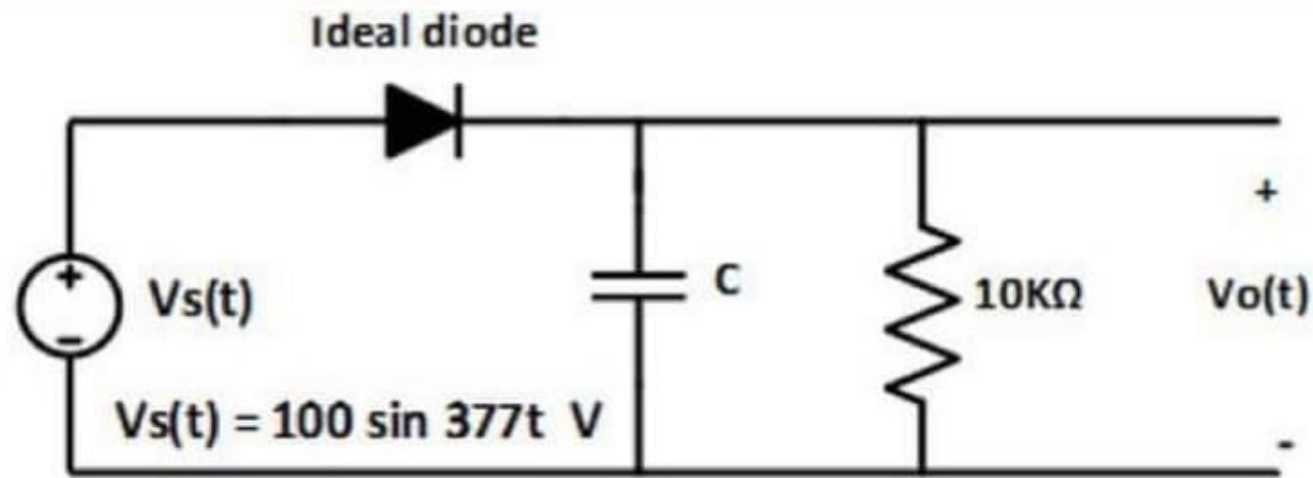
✓

μF



For the circuit shown, the value of the capacitor C that will result in a peak to peak ripple of 1V is

μF



The diodes in the circuit shown have $V_K = 0.7V$

$I_x =$ mA

